CEDA
IEEE Council on Electronic Design Automation

CEDA Launches Student Travel Grants Program

IEEE CEDA is happy to announce a student travel grants program to improve the representation of students to events sponsored by CEDA. The student travel grants can be awarded to any student attending a CEDA-sponsored conference, symposium or workshop. The grants are allocated based on the clear need of support to the requested students, and every year between 10-15 travel grants will be awarded, according to the number of applicants and the available program funding. The application should satisfy specific conditions described here.

The maximum request allowed for any Student Travel Grant is up to $1,500 that can be requested for inter-continental travels and $1,000 for intra-continental travels. The complete list of conferences sponsored by IEEE CEDA, which students can apply for, is provided here. Priority for consideration is given to applicants who are:

i. graduate or undergraduate students with accepted papers at the event,
ii. coming from developing countries (as listed by IEEE),
iii. have secured matching funds from their institutions, and
iv. have not previously received an IEEE CEDA Student Travel Grant.

An IEEE CEDA Student Travel Grant can only cover an early registration fee, and according to the needs and available funds, lodging and airfare. Applications must be submitted at least 60 days before the event takes place. Awards are announced no later than 45 days before the conference to reduce airline and registration costs. All IEEE CEDA Student Travel Grants must be submitted electronically on the IEEE CEDA Website using the CEDA Student Travel Grant Form.

CEDA Congratulations to Dr. Mary Jane Irwin

Mary Jane Irwin, Evan Pugh Professor and A. Robert Noll Chair Emeritus in Engineering in the Department of Computer Science and Engineering at Pennsylvania State University, has been selected to receive the 2019 Phil Kaufman Award for Distinguished Contributions to Electronic System Design.

Dr. Irwin is being honored for her extensive contributions to EDA through her technical efforts, service to the community and leadership. During her tenure at Pennsylvania State University, she mentored countless students and contributed to technology through her substantial research and numerous publications. Her research included creating EDA tools then using them in computer architecture research, an approach that gave Dr. Irwin influence in both academia and industry.

The Phil Kaufman Award honors individuals who have had a demonstrable impact on the field of electronic system design through technology innovations, education/mentoring, or business or industry leadership. The award was established as a tribute to Phil Kaufman, the late industry pioneer who turned innovative technologies into commercial businesses that have benefited electronic designers. The Phil Kaufman Award is presented annually by the Electronic System Design Alliance (ESD Alliance) and the IEEE Council on Electronic Design Automation (CEDA). The award ceremony and dinner was held on Thursday, November 7, at The GlassHouse in San Jose, California.

DAC 2019 – Call for Contributions

For 57 years, the Design Automation Conference (DAC) has been recognized as the leading-edge conference on research and practice in tools and methodologies for the design and design automation of electronic circuits and systems. DAC offers outstanding training, education, exhibits and networking opportunities for a worldwide community of designers, researchers, tool developers and vendors.

The Technical Program Committee for DAC 2020 is soliciting high-quality submission on design research, design practices and design automation for cross-cutting topics in the following areas: Electronic Design Automation (EDA), Embedded Systems and Software (ESS),

Design, Machine Learning/AI, Intellectual Property (IP), Autonomous Systems, Security/Privacy and Cloud-based Design.

Submissions are invited for Special Sessions, Designer Track, IP and Embedded Systems Track papers and presentations, poster sessions, panels, workshops, tutorials and co-located conferences. Criteria, topics and deadlines for the major tracks are outlined briefly below. Focused session topics are outlined briefly below. All submission details can be found at this link.

For more information on DAC's call for contributions, committees and other activities associated with the conference please visit the conference website www.dac.com. DAC is sponsored by the Association for Computing Machinery (ACM) and the Institute of Electrical and Electronics Engineers (IEEE), and is supported by ACM's Special Interest Group on Design Automation (ACM SIGDA).

Great Success for CEDA-sponsored Smart Cities Summit

A special 2019 IEEE full day summit was held on November 1st, in Austin at the Texas Advanced Computing Center (TACC) at J.J. Pickle Research Campus, Advanced Computing Building.

There were over 120 participants from different regions of the US and from out of countries such as Rome, Italy and Namibia, Africa, and remote connections were set up with Guadalajara, Mexico and Athens, Greece. The participants' profiles represent cities/municipalities, the medical community, industry and startups, as well as professors from multiple universities, researchers and students.

The theme of the summit was to examine the effect of disruptive technologies on potential innovative use cases for Smart Cities, Mobility and Healthcare. Disruptive technologies being considered include IoT, 5G, and analytics AI/ML/DL.

The summit included three keynote speakers and three panel sessions bringing the best of class examples and experience in the areas of mobility, healthcare and smart cities. The program included a keynote talk from IEEE

CEDA President Professor David Atienza on "Ultra-Low Power Wearables Systems: The Quest for Brain Efficiency," and panel sessions on healthcare and use cases where Prof. David Atienza and past IEEE CEDA President Dr Sani Nassif served as panelists.

SVDTC Activities Expression of Interest

IEEE System Validation and Debug Technology Committee (SVDTC) is a body of professionals from industry and academia encompassing different organizations across the globe. This IEEE committee grew out of the observation that system validation and debug still lacks an organized effort to address the industry challenges. To this end, the SVDTC's primary focus is on fostering innovation in the field of system validation tools/techniques, and post-implementation debug (on silicon prototypes or emulation), where the system encompasses hardware, firmware and/or system software. The aim is to tackle broad issues that are deemed beneficial to multiple partner organizations, institutions and thus have an industry-level impact on standards, specifications, problem definitions, and so on. SVDTC is also interested to help develop a system validation curriculum to establish this field as an academic discipline and therefore adequately prepare the next generation of electrical and computer engineers. Further information about SVDTC can be found here.

SVDTC (IEEE System Validation Debug Technology Committee) is conducting SVDTC Research Awards and details of the "Call for Submissions for the 2019 SVDTC Research Awards" is given in this link:

SVDTC (IEEE System Validation Debug Technology Committee) would like to solicit team members from Industry as well as Academia to attend SVDTC weekly meetings for the following efforts that we are pursuing:

- SVDTC SoC Scan and Memory (Array) Dump (Extraction) Debug Standards efforts
- SVDTC SoC Debug Tracing Standards efforts
- SVDTC Validation Coverage efforts

For expressing your interest and participating in these exciting activities, email SVDTC Contact.

Find us online at ieee-ceda.org.

IEEE Embedded Systems Letters is open for submissions.
Visit ieee-ceda.org/publication/esl-publication/author-guidelines.

IEEE Design & Test is open for submissions.
Visit ieee-ceda.org/publication/ieee-design-test-dt/paper-submission-instructions.