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2020 Phil Kaufman Award - Call for Nominations

Do you know someone who has made an outstanding contribution to Electronic System Design?

Nominations are being accepted for the 2020 Phil Kaufman Award! This award, presented annually by the Electronic System Design Alliance and the IEEE Council on Electronic Design Automation, honors an individual who has had a demonstrable, lasting impact on electronic design through contributions in the field of Electronic System Design.

The nominees' contributions should fall into one or more of the following categories:

- Business impact
- Industry direction and promotion
- Technology and engineering
- Education and mentoring

The nomination deadline is **June 30, 2020**. For information about previous recipients of this prestigious award and the 2020 nomination form click [here](#).

IEEE CEDA Ernest S. Kuh Early Career Award - Call for Nominations

This award honors an individual who has made innovative and substantial technical contributions to the area of Electronic Design Automation in the early stages of his or her career. The nomination deadline is **April 15**.

ACM SIGDA/IEEE CEDA Ph.D. Forum at DAC 2020

The [Ph.D. Forum](#) at the Design Automation Conference is a poster session hosted by ACM SIGDA and IEEE CEDA for Ph.D. students to present and discuss their dissertation research with people in the EDA community. It has become one of the premier forums for Ph.D. students in design automation to get feedback on their research and for industry to see academic work in progress: hundreds of people attended the last forums. Participation in the forum is competitive with acceptance rate of around 30%. Limited funds will be available for travel assistance, based on financial needs. The forum is open to all members of the design automation community and is free-of-charge. It is co-located with DAC to attract the large DAC audience, but DAC registration is not required in order to attend this event.

Topics of interest include but are not limited to: System-level Design, Synthesis and Optimization, Internet of Things (IoT), Autonomous Systems, High Level Synthesis, Logic Level Synthesis, Power and Reliability Analysis and Optimization, Timing Analysis, Circuit and Interconnect Simulation, Physical Design and Manufacturability, Signal Integrity and Design Reliability, Verification, Testing, Pre- and Post-Silicon Validation, Failure Analysis, Reconfigurable and Adaptive Systems, Analog/Mixed

Signals and RF, Hardware Security, Machine learning/AI, Printable and flexible hybrid electronics (FHE), Emerging Design, Technologies, and Computing Methods.

Guidelines for the two-page PDF abstract of the dissertation can be found [here](#) and submitted [here](#). For further questions, please e-mail [Dr. Ogras](#) or [Dr. Topaloglu](#). Please include "DAC Ph.D Forum" in the subject line of your email. The submission deadline is **April 15, 2020**.

20th International Low Power Design Contest at ISLPED 2020

The International Symposium on Low Power Electronics and Design ([ISLPED](#)) is holding the International Low Power Design Contest to provide a forum for universities and industry to showcase original "power-aware" designs and system solutions to highlight the design innovations targeting power and energy-efficiency improvement. The goal is to encourage and highlight design-oriented approaches for power reduction.

The best designs will be selected and invited for presentations/demo at ISLPED 2020. A special session in the symposium will be devoted to the Low Power Design Contest. Awards will be given for the best designs and demonstrations from industry and academia based on the on-site demonstration and presentation.

The 2-page proposal (without a cover page) should be submitted electronically via the ISLPED submission website, and a cover page (must be in a separate PDF file) should be submitted to the design contest co-chairs Pascal Meinerzhagen (pascal.a.meinerzhagen@intel.com) and Ajay Joshi (joshi@bu.edu). Acceptance and rejection notices will be emailed to the contact author by June 12, 2020. For more information and detailed submission guidelines, see the design contest [PDF](#) on the conference website. The deadline for submission of demo proposals is **11:59PM PST on May 15, 2020**.

ICCAD CAD Contest 2020

The [CAD Contest](#) at [ICCAD](#) is a challenging, multi-month, research and development competition, focusing on advanced, real-world problems in the field of Electronic Design Automation (EDA). It is open to multi-

person teams worldwide. Each year the organizing committee announces three challenging problems in different topic areas provided by industrial companies. Contestants can participate in one or more problems. The prizes will be awarded at an ICCAD special session dedicated to this contest. Since its inaugural year of 2012, the CAD Contest at ICCAD has been attracting more than a hundred teams per year, fostering productive industry-academia collaborations, and leading to hundreds of publications in top-tier conferences and journals. The contest undoubtedly boosts EDA research and keeps enhancing its impact. The registration deadline is **May 8, 2020**.

28th IFIP/IEEE International Conference on Very Large Scale Integration

VLSI-SoC 2020 is the 28th in a series of international conferences sponsored by the International Federation for Information Processing Technical Committee 10 Working Group 5, IEEE CEDA and IEEE CASS, which explore the state-of-the-art in the areas of Very Large Scale Integration (VLSI) and System-on-Chip (SoC) design. The purpose of [VLSI-SoC](#) is to provide a forum to exchange ideas and showcase academic as well as industrial research in architectures, circuits, devices, design automation, verification, test, and security, within digital, analog, and mixed-signal systems. VLSI-SoC 2020 will be held under the theme "Open Source VLSI Technologies" by addressing cutting-edge research fields like open-hardware processors and digital designs, digital and analog open-source electronic design automation tools and frameworks, etc.

Research topics of interest include, but are not limited to: Analog, Mixed Signal, Sensors and RF; VLSI Circuits and SoC Design; Embedded Systems Design & Software; CAD Tools and Methodologies; Verification, Modeling and Prototyping; Verification, Modeling and Prototyping; Design for Testability, Reliability and Fault Tolerance; Hardware Security; and Emerging technologies and new computing paradigms. More information can be found [here](#). The deadlines for abstract registration and paper submission are **April 20** and **April 27, 2020**, respectively.

Find us online at ieee-ceda.org.

IEEE Embedded Systems Letters is open for submissions.
Visit ieee-ceda.org/publication/esl-publication/author-guidelines.

IEEE Design & Test is open for submissions.
Visit <https://ieee-ceda.org/publication/ieee-design-test-dt/paper-submission-instructions>.



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