



### ***CEDA Early Career Award at ICCAD 2014***

The IEEE CEDA Early Career Award honors an individual who has made innovative and substantial technical contributions to the area of electronic design automation (EDA) in the early stages of his or her career.

The winner of this year's award, presented at the 2014 International Conference on Computer-Aided Design (ICCAD), is Shobha Vasudevan (University of Illinois at Urbana-Champaign) for his outstanding contributions to design verification, including automatic invariant generation, coverage analysis, timing verification, and analog verification.

### ***Winner of 2014 Phil Kaufman Award***

Lucio Lanza (managing director of Lanza Tech Ventures) has been selected by the EDA Consortium (EDAC) and CEDA as the recipient of the 2014 Phil Kaufman Award for his substantial impact on EDA through his strategic and financial assistance to innovative EDA companies.

EDA and IP startups have always been a key source of technology innovation. (Even today's larger EDA and IP companies were startups at one point.) Over the past two decades, Lanza has helped guide numerous startups as they have developed the innovative technologies needed to keep the industry moving forward.

His continuing strong support of the industry has included valuable business and technical mentoring based on his years of experience in the EDA and semiconductor industries, as well as financial backing in many cases.

Lanza received his doctorate in electronic engineering from Politecnico di Milano. He spent more than 20 years in industry, including Olivetti, Intel, and Daisy Systems, before joining the ranks of venture capitalists in 1990 at US Venture partners. In 2001, he founded Lanza Tech Ventures, an early-stage venture-capital and investment firm.

In addition to his industry and financial experience, Lanza served as a visiting professor at Carnegie Mellon University from 2002 to 2006. He has also been a regular contributor to major industry conferences, including the Design Automation Conference (DAC) and ICCAD.

We are pleased to announce that, in recognition of his advancement of the EDA and IP industries through his generous business and technical mentoring, as well as his financial support of numerous innovative companies, the winner of this year's prestigious Phil Kaufman Award is Lucio Lanza.

### ***CEDA Central Texas Section Chapter Update***

The IEEE CEDA Central Texas Section Chapter (CTS-CEDA) was founded in 2012. Current officers include Founding Chair Zhuo Li (Cadence Design Systems), Vice Chair Magdy Abadir, Secretary Cliff Sze (IBM), Treasurer and Webmaster Natarajan Viswanathan (IBM), and vTools Coordinator Norma Antunano (HP).

CTS-CEDA has organized eight seminars in 2014. Speakers have included industry executives and experts from IBM, LSI, Freescale Semiconductor, Xcelomor, the Silicon Integration Initiative, and the University of California, Santa Barbara, as well as a registered patent agent.

### ***TexasWISE 2014***

This year, CTS-CEDA co-organized the 2014 Texas Workshop on Integrated System Exploration (TexasWISE), held on 21 March 2014 in Austin.

Other organizers of this workshop included IBM, the IEEE Circuits and Systems Society and the IEEE Solid-State Circuits Society, Rice University, Texas A&M University, the University of Texas at Austin, the University of Texas at Dallas, and Southern Methodist University.

### ***IEEE Workshop on Employee, Consultant, Entrepreneur – Skills for a Multi-faceted Career***

On 13 September 2014, CTS-CEDA also cohosted a career management workshop, along with six other local IEEE chapters. Today's most successful engineers likely

will perform many different roles during the course of their careers. Regardless of what career stage they are in, personal-interaction skills can enhance their ability to accomplish their goals.

During this full-day workshop, six nationally recognized speakers shared the experiences and knowledge they have gained during their individual, wide-ranging careers.

#### Learn More about CTS-CEDA

CTS-CEDA has established a mailing list ([ceda-texas@listserv.ieee.org](mailto:ceda-texas@listserv.ieee.org)) with more than 1,700 recipients. CTS-CEDA also has a LinkedIn Page (search for IEEE CTS CEDA Chapter).

To learn more about CTS-CEDA, please contact Zhuo Li ([zhuoli@ieee.org](mailto:zhuoli@ieee.org)) or visit our website at [http://ewh.ieee.org/r5/central\\_texas/ceda](http://ewh.ieee.org/r5/central_texas/ceda).

We welcome your feedback and suggestions to further improve our chapter.

*Zhuo Li, CTS-CEDA Chair*

#### IEEE Design & Test Call for Papers

*IEEE Design & Test* presents original works on methods used to design and test electronic-product hardware and supportive software. The magazine focuses on current and near-future practice, and includes tutorials, how-to articles, and real-world case studies.

Topics include IC and module design, low-power design, EDA, design and test verification, practical technology, and standards.

Please consider submitting articles of general interest with 4,500 to 5,000 words to *IEEE Design & Test* at <https://mc.manuscriptcentral.com/dandt>.

More information on *IEEE Design & Test* is available at <http://ieeeced.org/publications/d-t>.

#### Papers in IEEE Embedded Systems Letters

The top-five accessed articles from *IEEE Embedded Systems Letters* in July 2014 were as follows:

- “[A Security Layer for Smartphone-to-Vehicle Communication over Bluetooth](#),” by A. Dardanelli et al.
- “[Runtime Adaptation of Applications Using Design of Experiments: A Smartphone-Based Case Study](#),” by F. Maker, R. Amirtharajah, and V. Akella
- “[An FPGA-Based Plant-on-Chip Platform for Cyber-Physical System Analysis](#),” by S. Vyas et al.
- “[High Communication Throughput on Low Scan Cycle Time with Multi/Many-Core Programmable Logic Controllers](#),” by A. Cane-do, H. Ludwig, and M.A. Al Faruque
- “[SIMON Says: Break Area Records of Block Ciphers on FPGAs](#),” by A. Aysu, E. Gulcan, and P. Schaumont

Upcoming Conferences (David Atienza, <a href="mailto:david.atienza@epfl.ch">david.atienza@epfl.ch</a> )	
<a href="#">VLSI Design</a>	Bangalore, India, 3-7 Jan. 2015
<a href="#">ASP-DAC</a>	Tokyo, 19-22 Jan. 2015
<a href="#">DATE</a>	Grenoble, France, 9-13 Mar. 2015
<a href="#">DAC</a>	San Francisco, 7-11 June 2015

**IEEE Embedded Systems Letters is open for submissions. Visit [mc.manuscriptcentral.com/les-ieee](https://mc.manuscriptcentral.com/les-ieee)**

#### IEEE COUNCIL ON ELECTRONIC DESIGN AUTOMATION

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*CEDA Currents* is a publication of IEEE CEDA. Please send contributions to Jose L. Ayala ([jayala@fdi.ucm.es](mailto:jayala@fdi.ucm.es)).

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