

CEDA Distinguished Speaker Videos Available

IEEE CEDA announces online videos of talks by distinguished speakers, available at <http://www.ieee-ceda.org/lectures.html>. Recent additions include a talk by Frances A. Houle of the IBM Almaden Research Center on professional ethics, one by Janusz Rajski on embedded deterministic test, and Zhenhai Zhu's "A Fast Stochastic Integral Equation Solver for Modeling the Rough Surface Effects."

IEEE Offers Presubmission Professional Editing Services

IEEE Publishing Operations has partnered with SPi Publisher Services, to offer presubmission professional editing services to authors submitting a manuscript to an IEEE publication. SPi provides scientific, technical, and medical publishers with a high-quality, end-to-end outsourcing solution. Their multicountry delivery platform harnesses the talent of more than 3,500 content and business-process-outsourcing specialists. Located in the US, Europe, and the Philippines, these specialists copy-edit more than 1 million pages per year for over 600 journals. SPi has provided content tagging and editing services to IEEE Publishing Operations since 2002.

Any author who would like assistance with English grammar and usage prior to submitting their manuscript to an IEEE publication for review or during the review process can now go directly to <http://www.prof-editing.com/ieee>. The SPi copy editors will edit your document for grammar, usage, organization, and clarity, querying you about potentially substantive revisions as necessary. An author can use the service, at his or her own expense, as often as desired. Cost estimates are available immediately online. Edited manuscripts will generally be returned to the author within two weeks of submission. (Manuscripts accepted in an IEEE publication such as *IEEE Design & Test* will still be edited in accordance with the author style guidelines and editorial requirements of that publication. In addition, please note that this service cannot guarantee that your manuscript will be accepted for publication.)

IEEE Fellows Elected in 2006 for CEDA-Related Activities

These individuals have been elected as IEEE Fellows for contributions in the following CEDA-related areas.

- *Luca Benini, University of Bologna, Bologna, Italy:* Design technologies for low-power design of integrated circuits and systems
- *Bhargab B. Bhattacharya, Indian Statistical Institute, Calcutta:* Testing and design of digital ICs
- *Abhijit Chatterjee, Georgia Institute of Technology, Atlanta:* Testing of analog and mixed-signal circuits
- *Anirudh Devgan, Magma Design Automation, Austin:* Electrical analysis, and simulation of ICs
- *Kenneth S. Kundert, Designer's Guide Consulting, Los Altos:* Simulation and modeling of analog RF and mixed-signal circuits
- *Sandip Kundu, University of Massachusetts, Amherst:* Design of IC test methods
- *Gaetano Palumbo, University of Catania, Catania, Italy:* Analysis and design of high-performance analog and digital circuits
- *Ruchir Puri, IBM Thomas J. Watson Research Center, Yorktown Heights:* Automated logical and physical design of electronic circuits
- *Jose Schutt-Aine, University of Illinois, Urbana:* Modeling and simulation of distributed circuits with applications to signal integrity

This is not a complete list of the 2007 IEEE Fellows. We apologize in advance for any omissions. Please see <http://www.ieee.org/web/aboutus/fellows/new-fellows.html> for the complete list.

Nominations Invited for 2007 IEEE Fellows

The 2007 IEEE Fellow nomination process is in progress, and nominations and supporting materials are due 1 March 2007. You can nominate someone electronically at <http://www.ieee.org/web/aboutus/fellows/request.html>. Please note that this year CEDA is a "Society/Council to Evaluate Fellow Candidates" and can be selected on the nomination form.

Nominees must be IEEE senior members and must have completed five years of IEEE membership. You are urged to consider nominating outstanding candidates in the design technology area. Anyone (except those involved in the evaluation) can submit a nomination.

IEEE TAB Approves Council Cosponsorship of the IEEE Kaufmann Award

The IEEE Technical Activities Board (TAB) approved CEDA sponsorship of the Phil Kaufman Award for Distinguished Contributions to Electronic Design Automation jointly with the EDA Consortium. This award honors an individual who has significantly impacted the field of electronic design through contributions in electronic design automation (EDA).

CEDA is also cosponsoring the Donald O. Pederson Award for the best paper in *IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems* and the William J. McCalla Award for the best paper at ICCAD, and provides nominations and input for the IEEE Piore Award. Together, these awards represent excellent mechanisms to recognize contributions from our community. Please send your suggestions and nominations to CEDA administrator Barbara Wehner at b.wehner@ieee.org.

Upcoming Events

Design, Automation and Test in Europe Conference (DATE)

16-20 April 2007

Nice, France

<http://www.date-conference.com>

16th International Workshop on Logic & Synthesis (IWLS)

30 May - 1 June 2007

San Diego, Calif.

<http://www.iwls.org>

Paper submissions deadline: 3 March 2007

5th ACM-IEEE International Conference on Formal Methods and Models for Codesign (MEMOCODE)

30 May - 1 June 2007

Nice, France

<http://memocode.irisa.fr>

7th International Forum on Application-Specific Multi-Processor SoC (MPSoC)

25-29 June 2007

<http://tima.imag.fr/mpsoc>

Hyogo, Japan

International Workshop on Power and Timing Modeling, Optimization and Simulation (PATMOS)

3-5 September 2007

Göteborg, Sweden

<http://www.ce.chalmers.se/research/conference/patmos07>

Paper submissions deadline: 7 March 2007

Contact: Lars Svensson, lars.svensson@chalmers.se

IEEE/ACM International Conference on Computer-Aided Design (ICCAD)

4-8 November 2007

San Jose, Calif.

<http://www.iccad.com>

Paper submissions deadline: 11 April 2007

For more information regarding sponsorship of conferences and meetings, contact Richard Smith, dsmith@topher.net.

CEDA Currents is a publication of the IEEE Council on Electronic Design Automation. Please send contributions to Kartikeya Mayaram (karti@eecs.oregonstate.edu) or Preeti Ranjan Panda (panda@cse.iitd.ac.in).