



IEEE Council on Electronic Design Automation

Attend the CEDA Distinguished Speaker Luncheon at DAC 2022

Each year, CEDA invites experts in EDA to present to the Design Automation Conference attendees. This year's IEEE CEDA Distinguished Speaker Luncheon is presented by Xiaobo Sharon Hu of the University of Notre Dame. The presentation will be held on 12 July from 12:00 pm – 2:30 pm PT.

Her presentation will be about data transfer between processors and memory is a major bottleneck in improving application performance on traditional computing hardware. This talk will showcase several representative cross-layer IMC based design efforts.

To learn more [click here](#).

Ph.D. Forum @ DAC 2022

The Ph.D. Forum at the [Design Automation Conference](#) is a poster session hosted by [ACM SIGDA](#) and [IEEE CEDA](#) for Ph.D. students to present and discuss their dissertation research with people in the EDA community. It has become one of the premier forums for Ph.D. students in design automation to get feedback on their research and to connect with other members of the community. It also enables both, academicians and industry, to see the best graduating students in one place.

Presentations are selected through a scientific evaluation by an expert committee consisting of academia and industry. The forum is open to all members of the design automation community and is free of charge. It is co-located with DAC, but a DAC registration is *not* required in order to attend this event.

To view the full schedule for DAC visit the [website](#).

2022 Awards Nominations Deadlines

Reminder Nomination Deadline: 30 June 2022
The Phil Kaufman Award for Distinguished Contributions to Electronic Design Automation is jointly sponsored by the [ESD-Alliance](#) and the IEEE Council on Electronic Design Automation and honors an

individual who has had a demonstrable impact on the field of electronic design through contributions in Electronic Design Automation.

Nominations are accepted on a rolling basis.

The [Distinguished Service Award](#) recognizes an individual who has provided outstanding service to the IEEE Council that has had a significant benefit to the EDA community.

2022 IEEE TCAD Donald O. Pederson Best Paper Award Winners

The [IEEE Transactions on Computer-Aided Design Donald O. Pederson Best Paper Award](#) is sponsored by the IEEE Council on EDA and recognizes the best paper published in the Transactions on Computer-Aided Design of Integrated Circuits and Systems publication.

The award is based on the overall quality, the originality, the level of contribution, the subject matter and the timeliness of the research. Anyone who is an author of a paper published in the Transactions on Computer-Aided Design of Integrated Circuits and Systems during the two calendar years preceding the award is eligible for nomination.

Congratulations to the authors— Bon Woong Ku, Kyungwook Chang, and Sung Kyu Lim— for the paper entitled “*Compact-2D: A Physical Design Methodology to Build Two-Tier Gate-Level 3-D ICs*”. The paper was published in the IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems (Volume: 39, Issue: 6, June 2020).

CAD for Assurance Webinar June 10

The CAD for Assurance webinar series is an educational initiative that provides CEDA members with access to relevant CAD tools to use. This includes information on major CAD tools the research community has developed over the past decade, including open-source license-free or ready-for-licensing tools, associated metrics, relevant publications, and video-demos.

The series will continue on 10 June 2022 with a panel presentation "Securing Crypto in the Pre & Post-Quantum Era: Opportunities and Challenges?" by Rosario Cammarota (Intel), Ramesh Karri (New York University), Sri Parameswaran (UNSW Sydney), Apostol Vassilev (NIST), Ingrid Verbauwhede (KU Leuven).

Registration is free for the webinars, however is required in order to attend. Additional information can be found on the CEDA CAD for Assurance [website](#).

Call for Papers: IEEE Journal on Exploratory Solid-State Computational Devices and Circuits special topic on "Energy-Efficient Compute-in-Memory with Emerging Devices"

Prospective authors are invited to submit original works and/or extended works based on conference presentations on various aspects of compute-in-memory. Memory technologies of interest include (but not limited to) SRAM, DRAM, eDRAM, NOR/NAND Flash, and emerging NVM devices such as PCM, RRAM/CBRAM, STT-MRAM/SOT-MRAM (or other spintronic memories), FeFET (or other ferroelectric memories), etc. View the call for papers [here](#).

Register for MPSoC'22

MPSoC is a world class forum bringing together 48 R&D top leaders from industry and academia to discuss fundamental and strategic issues to master Software and Hardware integration for better cost, performances and energy efficiency of next generation multicore and multiprocessor SoC (MPSoC). This year, MPSoC covers the key topics for **multicore** architectures targeting **AI and embedded computing** and including Hardware and software IP.

Registration [here](#).

Call for Interest in Recruitment Event

Embedded Systems Week (ESWEEK 2022) will host a virtual recruiting event in GatherTown to facilitate recruitment related networking in a hybrid/virtual setting. This recruiting event will take place during ESWEEK 2022. Exact date/time is to be announced.

Company representatives – if you are interested in hiring undergraduate, masters or Ph.D. students for full-time, part-time, or internship jobs in the

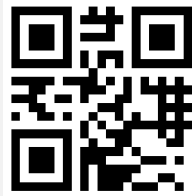
embedded systems and related areas, please send an email expressing your interest to Patricia Derler with the subject line "[ESWEEK 2022 Recruitment Event]". For more information about Embedded Systems Week visit the [ESWEEK website](#).

IWLS 2022 Programming Contest

The goal of this year's contest is to synthesize small circuits for completely-specified multi-output Boolean functions represented using truth tables. Participants should find competitive solutions for different benchmarks using a variety of novel break-through methods (e.g., search and enumeration, new decomposition, etc.). To participate, check out the [contest description](#) and download the [benchmark](#).

Please submit your solution by **20 June 2022**.

To learn more [click here](#).



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Please send contributions to Agnieszka Dubaj

 [Contributions Form](#)

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IEEE Embedded Systems Letters is open for submissions

Visit bit.ly/ESLsubmissions

IEEE Design & Test is open for submissions

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